

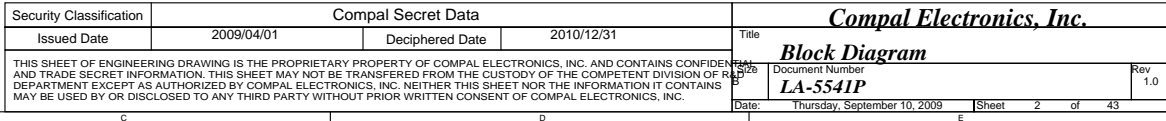
Compal Confidential

Schematic Document

Rev: 1.0

2009.09.10

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Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title	Cover Sheet	
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Model Name : NAT20

Board ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra	100K +/- 5%			
Board ID	Rb	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	18K +/- 5%	0.436 V	0.503 V	0.538 V
2	33K +/- 5%	0.712 V	0.819 V	0.875 V
3	56K +/- 5%	1.036 V	1.185 V	1.264 V
4	100K +/- 5%	1.453 V	1.650 V	1.759 V
5	200K +/- 5%	1.935 V	2.200 V	2.341 V
6	NC	2.500 V	3.300 V	3.300 V
7				

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	0.2
2	0.3
3	0.4
4	
5	
6	
7	

Symbol Note :



SMBUS Control Table

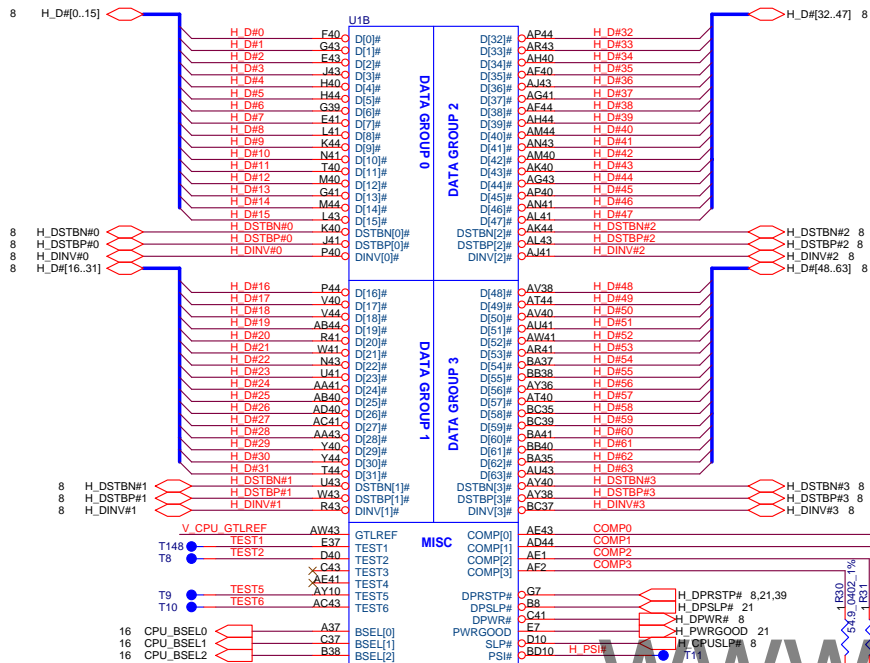
	SOURCE	INVERTER	BATT	SERIAL EEPROM	THERMAL SENSOR (CPU)	SODIMM
EC_SMB_CK1 EC_SMB_DA1	KB926	X	V	X	X	X
EC_SMB_CK2 EC_SMB_DA2	KB926	X	X	X	V	X
ICH_SMBCLK ICH_SMBDATA	ICH9	X	X	X	X	V
MCP_SMB_CLK MCP_SMB_DATA	ICH9	X	X	X	X	X

I2C / SMBUS ADDRESSING

DEVICE	HEX	ADDRESS
DDR SO-DIMM 0	A0	1 0 1 0 0 0 0
DDR SO-DIMM 1	A4	1 0 1 0 0 1 0 0
CLOCK GENERATOR (EXT.)	D2	1 1 0 1 0 0 1 0

MINI CARD 1 RESERVED +3VALW TO PULL HIGH

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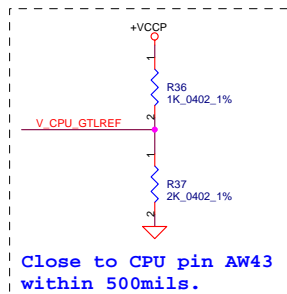


layout note: Route TEST3 & TEST5 traces on ground referenced layer to the TPs

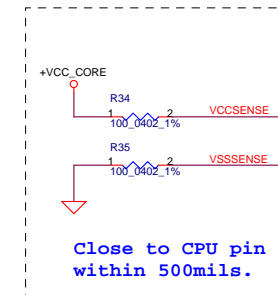
CPU_BSEL	CPU_BSEL2	CPU_BSEL1	CPU_BSEL0
166	0	1	1
200	0	1	0
266	0	0	0

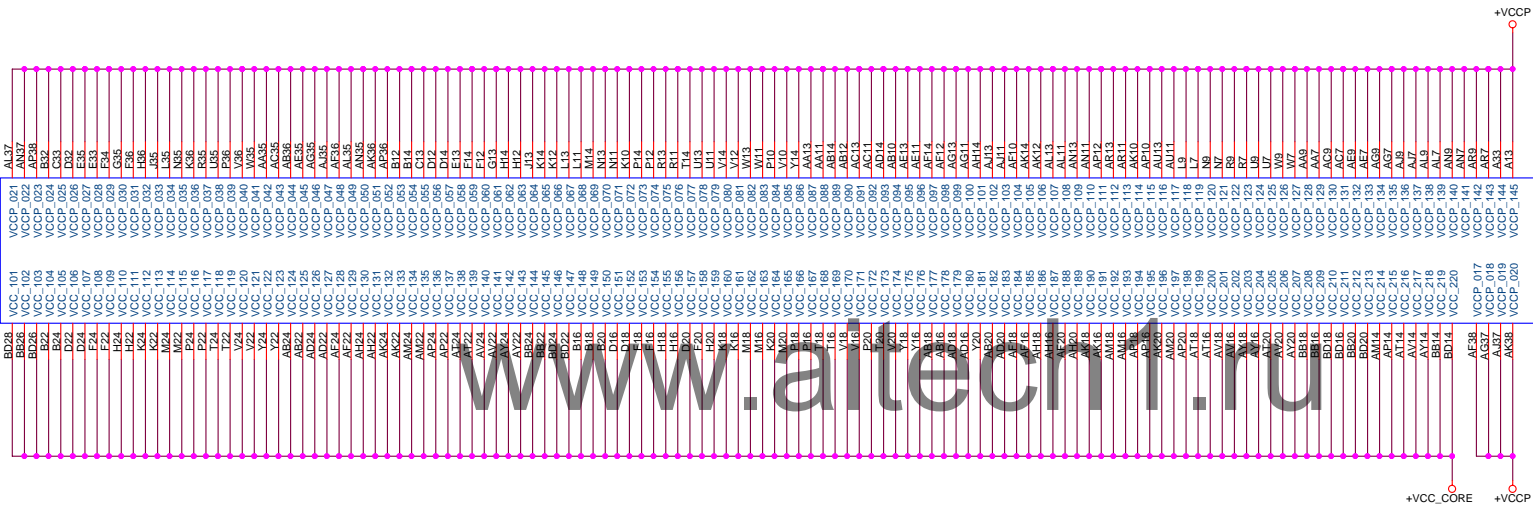
Cause CPU core power change to 1 phase, and not need support the pin, leave it as TP. 10/02

Resistor placed within 0.5" of CPU pin. Trace should be at least 25 mils away from any other toggling signal. COMP[0,2] trace width is 18 mils. COMP[1,3] trace width is 4 mils.



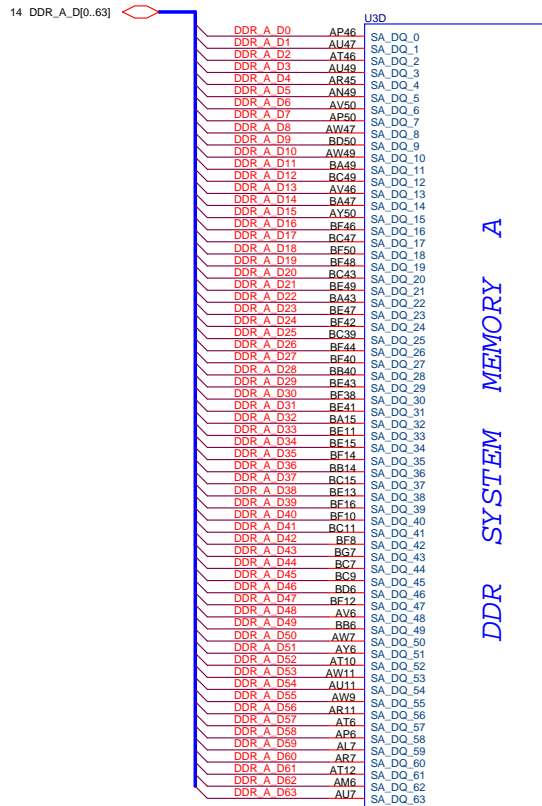
Length match within 25 mils. The trace width/space/other is 20/7/25.



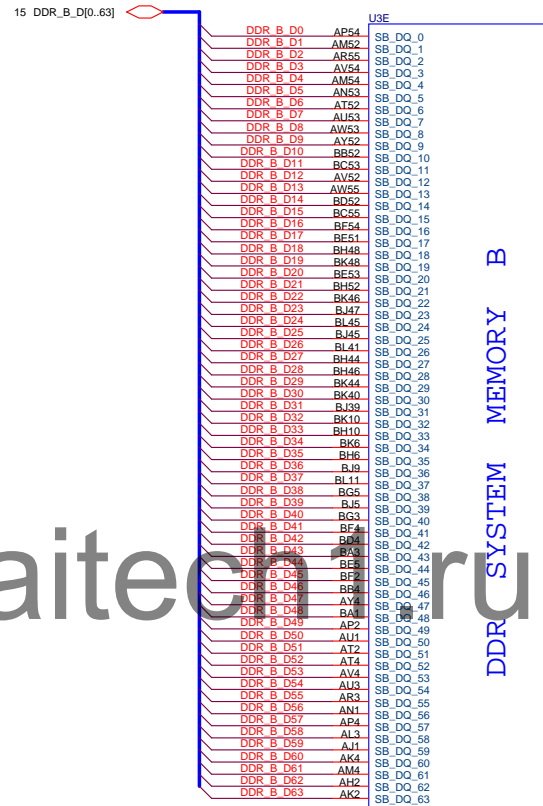


U1F
PENRYN SFF_UFCBGA956
SU2700®

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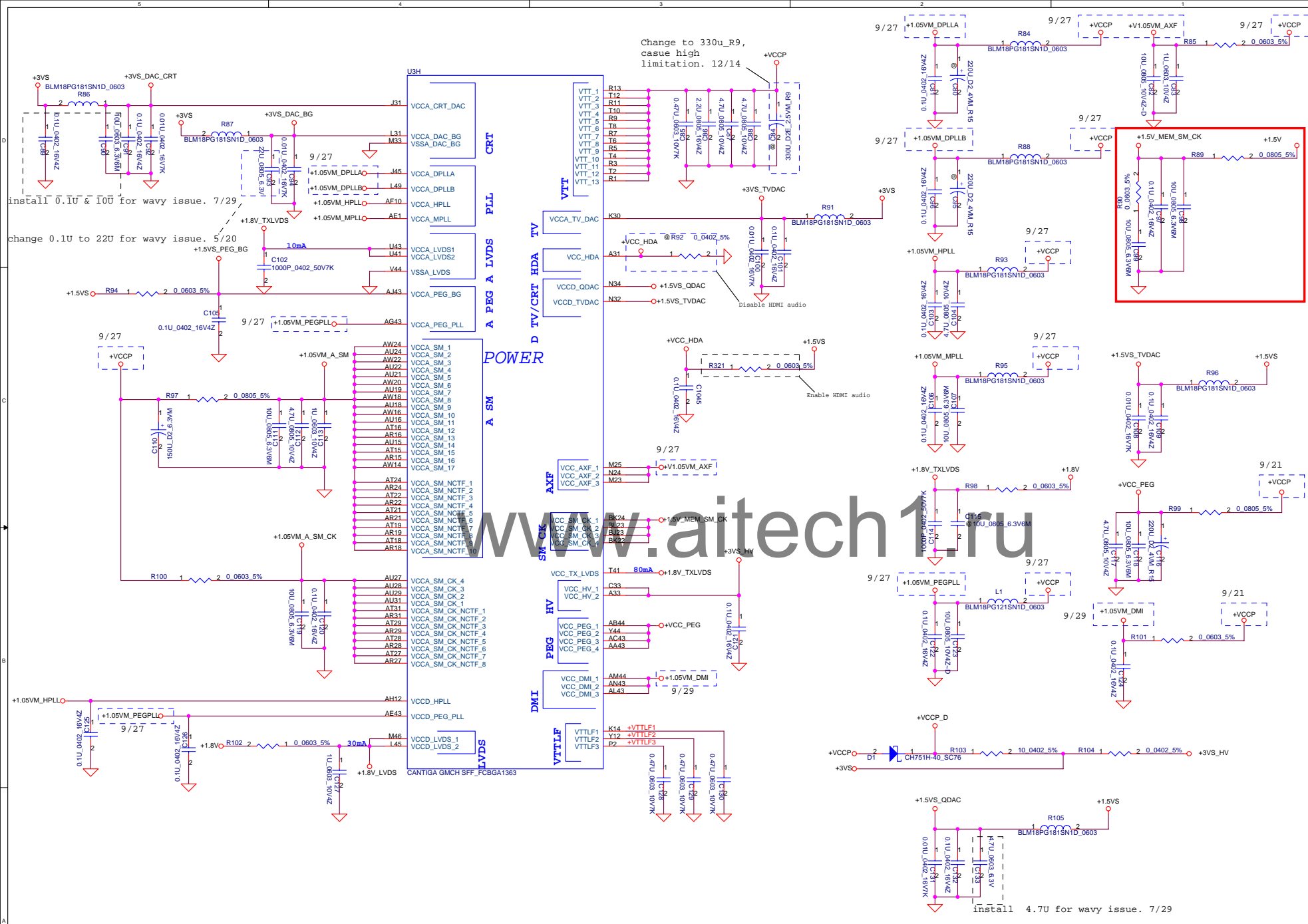


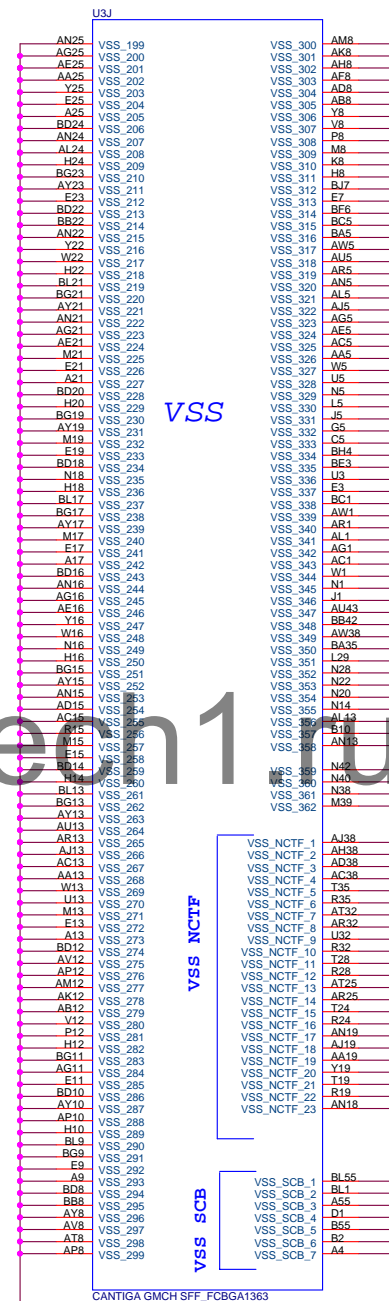
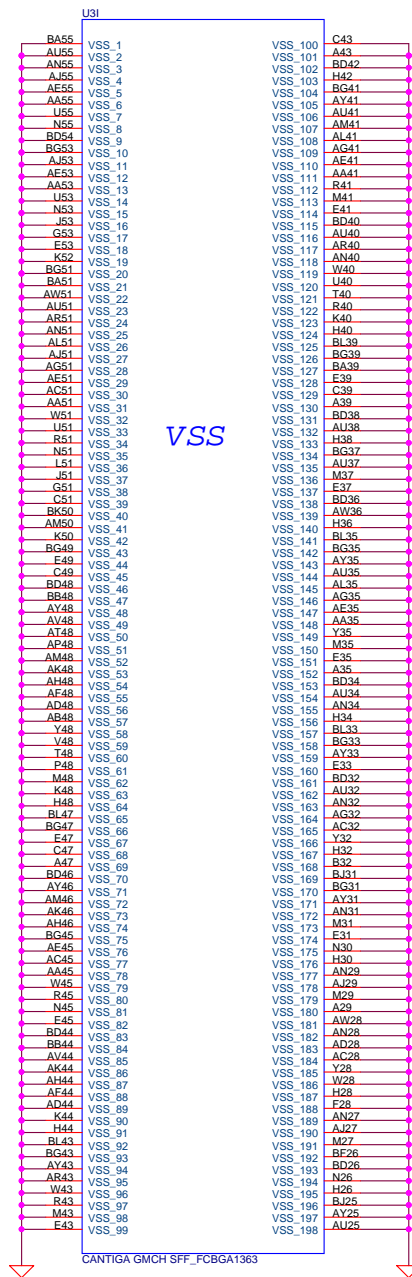
DDR SYSTEM MEMORY A



DDR SYSTEM MEMORY B

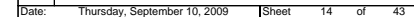
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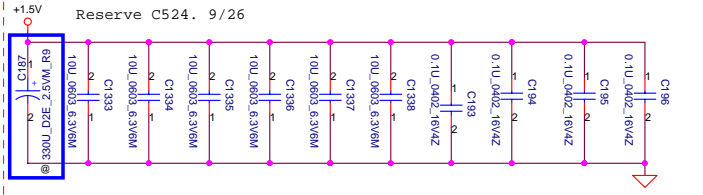
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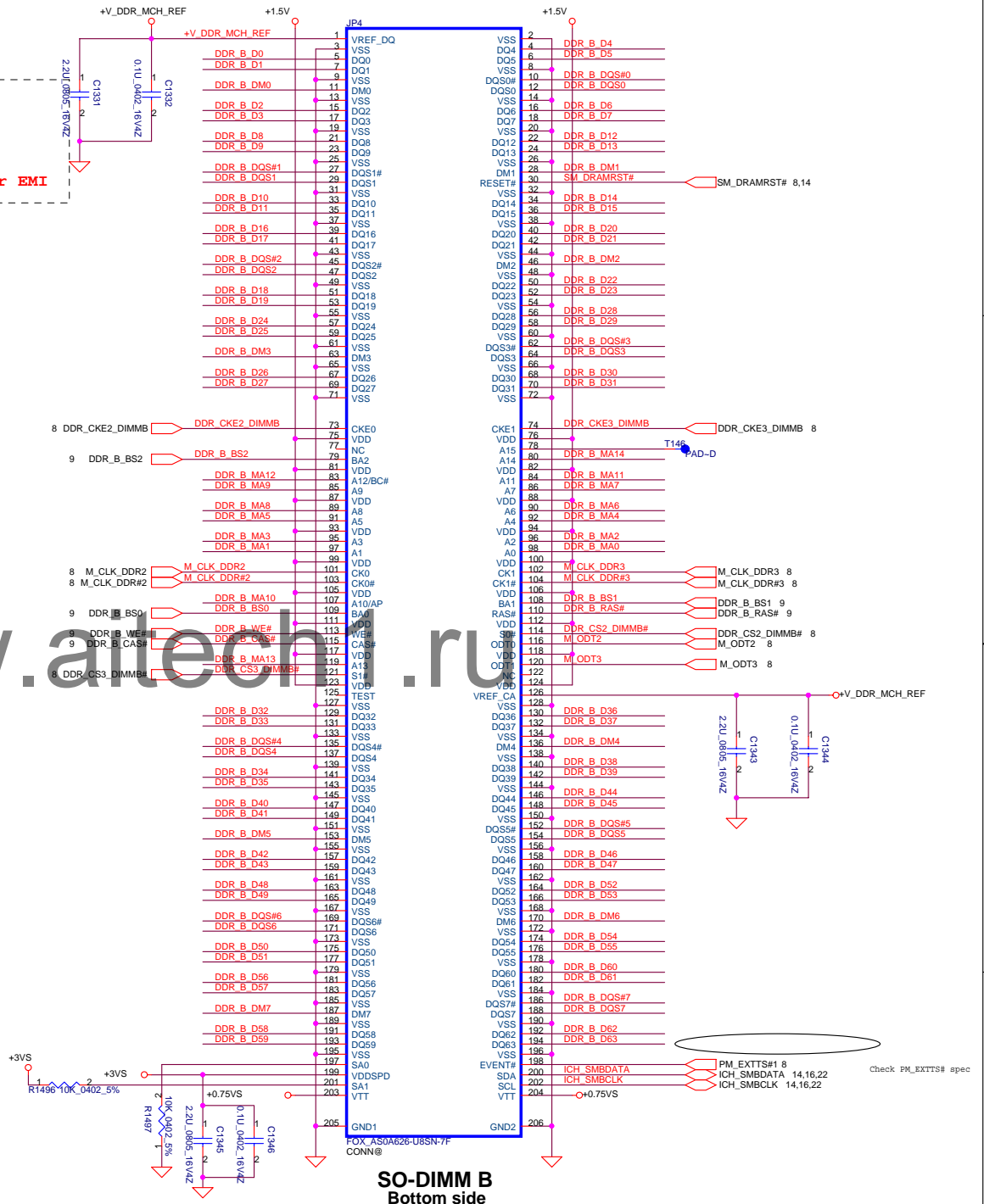
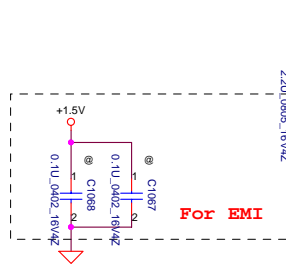
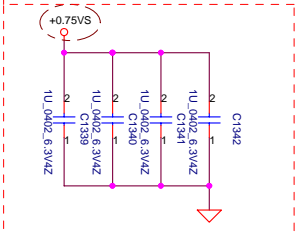


9 DDR_B_DQS#[0..7]
9 DDR_B_D[0..63]
9 DDR_B_DM[0..7]
9 DDR_B_DQS[0..7]
9 DDR_B_MA[0..14]

Layout Note:
Place near JP4



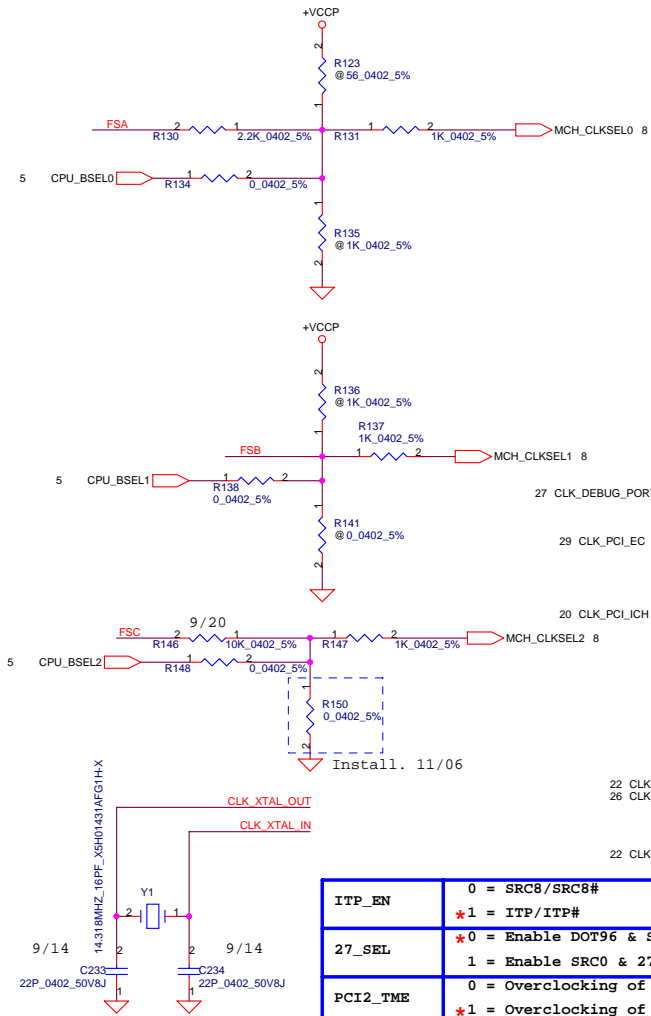
Layout Note:
Place near JP4.203,204



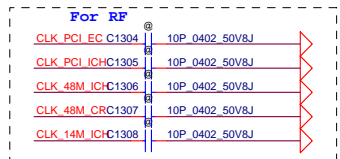
SO-DIMM B
Bottom side

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								DDR3 SO-DIMM Slot B	
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FSLC CLKSEL2	FSLB CLKSEL1	FSLA CLKSEL0	CPU MHz	FSB MHz	SRC MHz	PCI MHz
0	0	0	266	1066	100	33.3
0	1	0	200	800	100	33.3
0	1	1	166	667	100	33.3



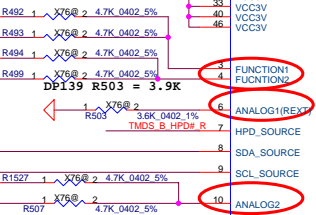
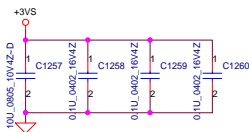
ITP_EN	0 = SRC8/SRC8# *1 = ITP/ITP#
27_SEL	*0 = Enable DOT96 & SRC1(UMA) 1 = Enable SRC0 & 27MHz(DIS)
PCI2_TME	0 = Overclocking of CPU and SRC Allowed *1 = Overclocking of CPU and SRC NOT allowed



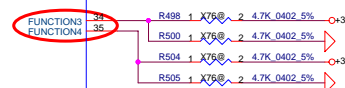
SA000020H10 for Compal
SA00001YJ20 for HP

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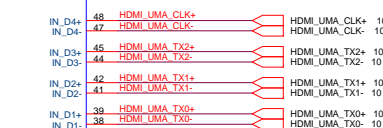
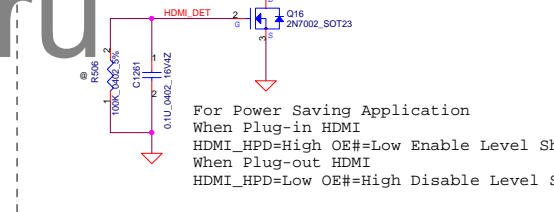
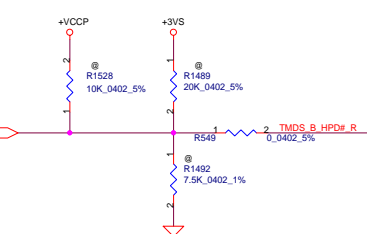
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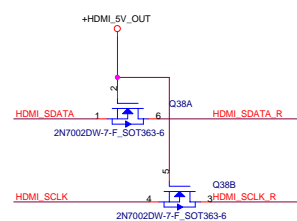
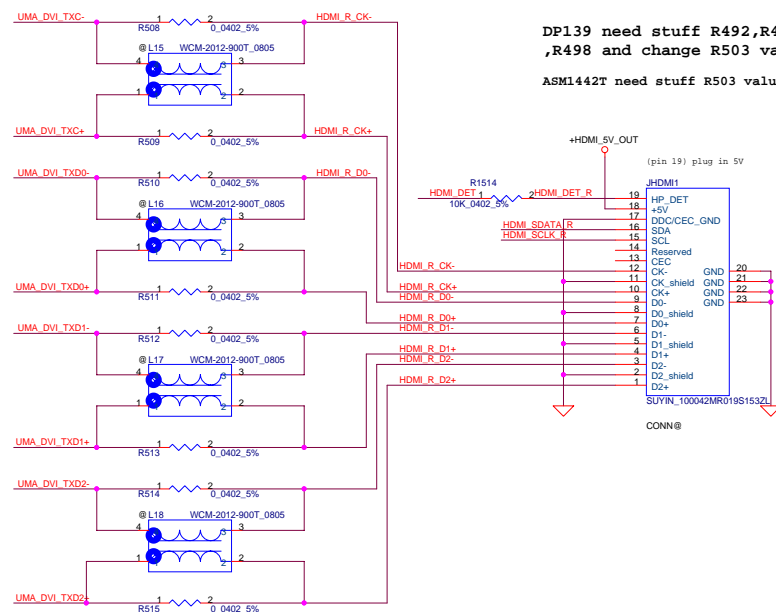
TMDS_B_CLK	PEG_TXP_3
TMDS_B_CLK#	PEG_TXN_3
TMDS_B_DATA0	PEG_TXP_2
TMDS_B_DATA0#	PEG_TXN_2
TMDS_B_DATA1	PEG_TXP_1
TMDS_B_DATA1#	PEG_TXN_1
TMDS_B_DATA2	PEG_TXP_0
TMDS_B_DATA2#	PEG_TXN_0
TMDS_B_HPD#	PEG_RXP_3



Pin34	Pin35	Equalization	Default
0	0	12dB	
0	1	9dB	
1	0	6dB	
1	1	3dB	*

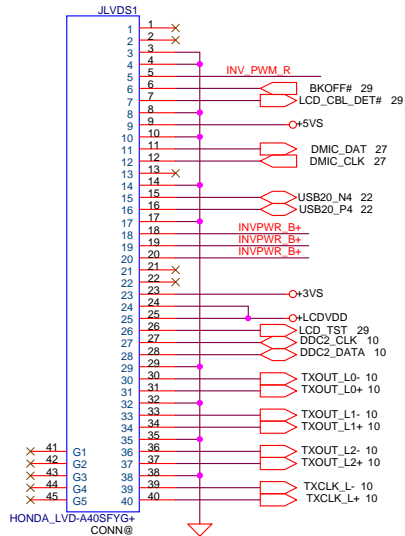
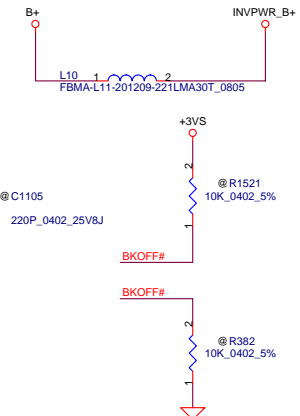
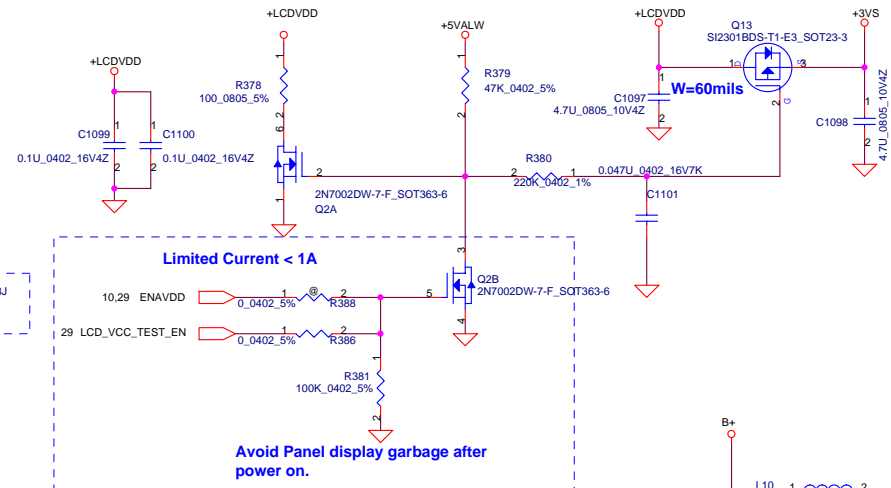
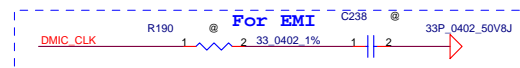
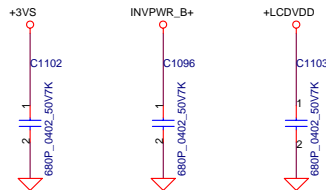
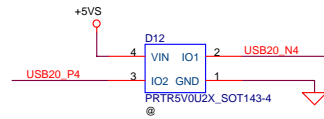
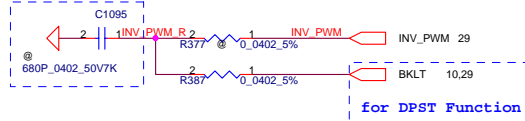
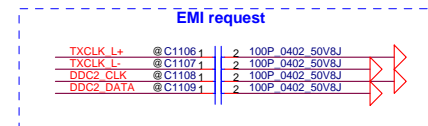
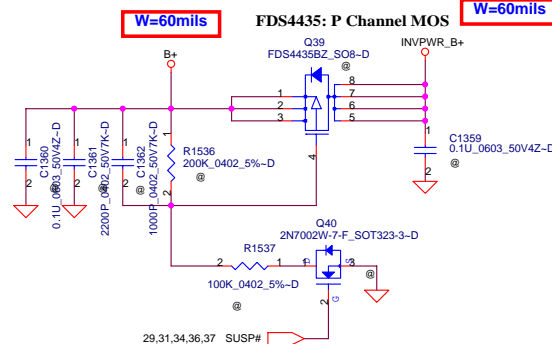


20071031:
Add U1. 49 (THERMAL_GND) to GND Plane

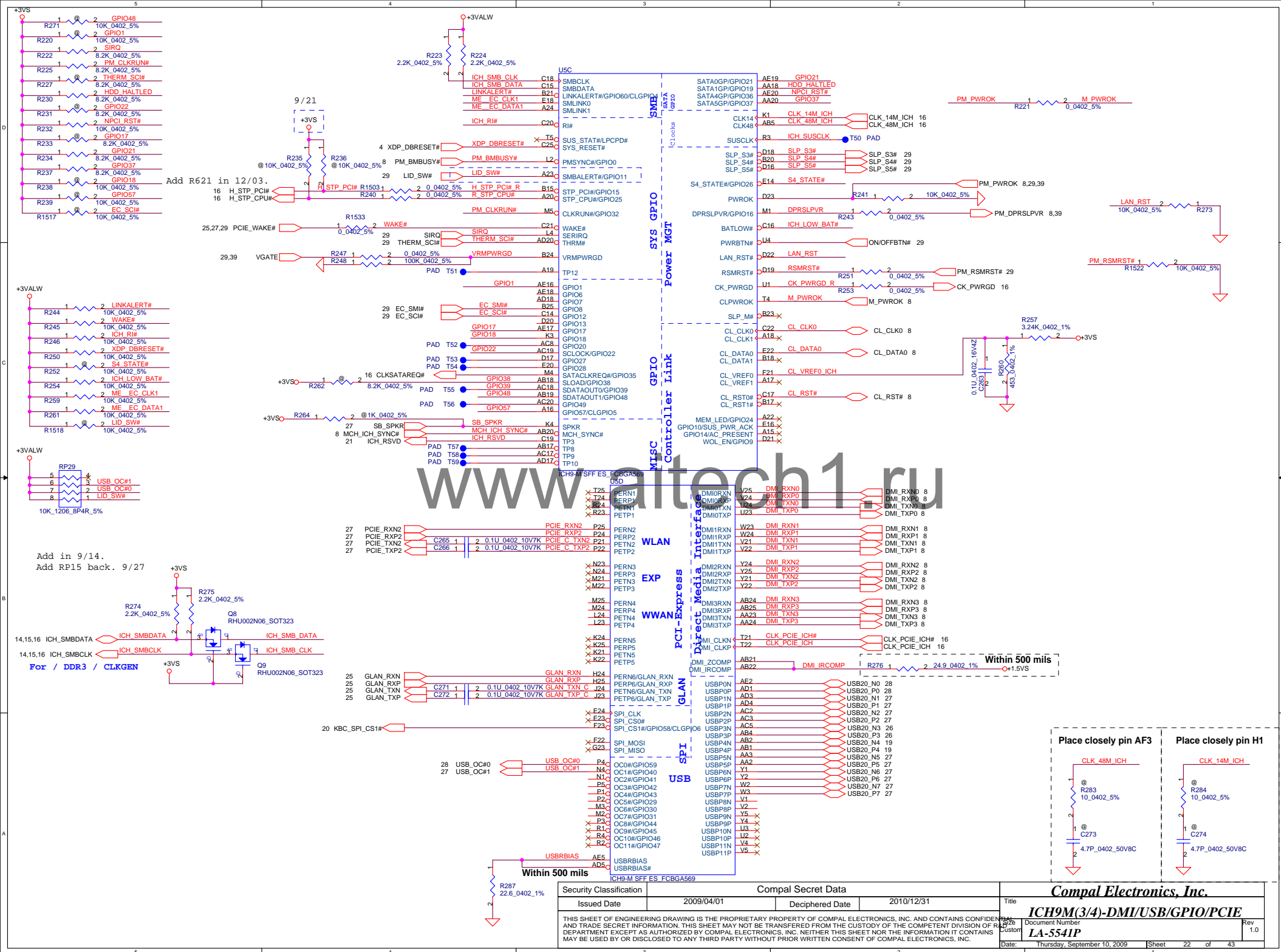


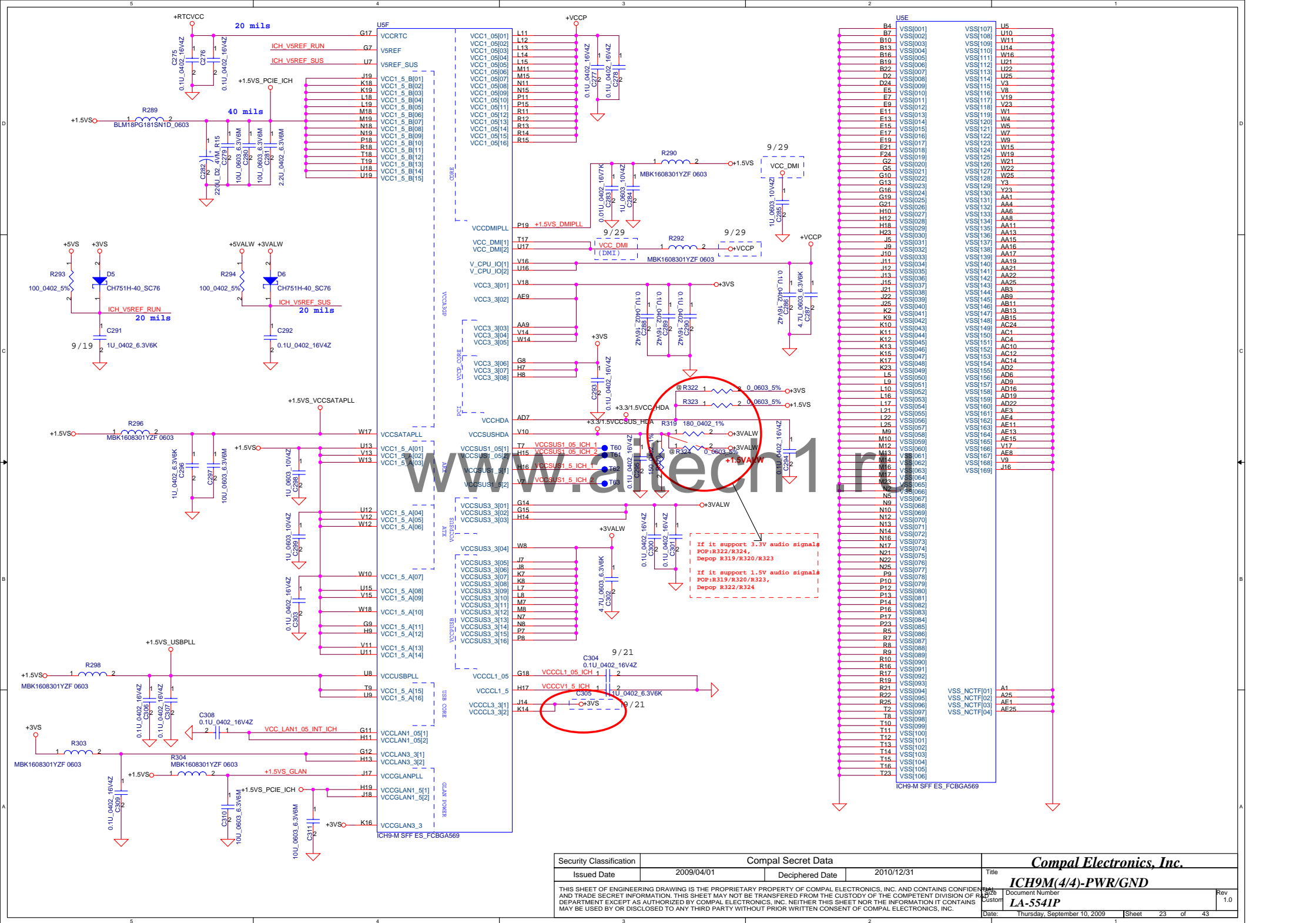
Pin 10	Pin 4	Pin 1	Swing	Pre-amp	Slew-rate	Default
0	0	0	450	0	0	
0	0	1	420	0	0	
0	1	0	450	0	0	
0	1	1	460	0	-4dB	*
1	0	0	340	0	0	
1	0	1	400	2dB	0	
1	1	0	400	2dB	0	
1	1	1	400	0	0	

EMI request.

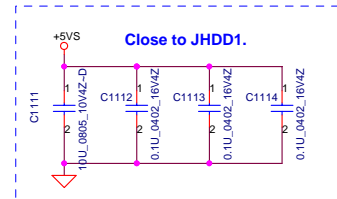
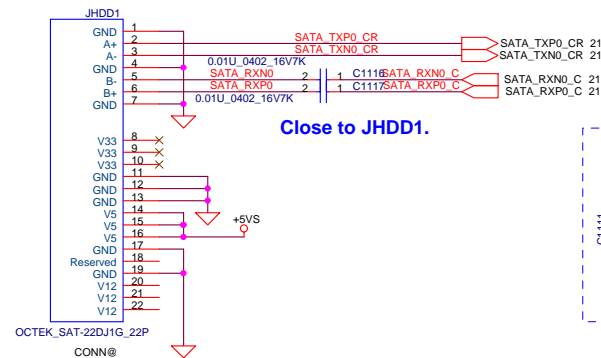
LVDS CONN & USB Camera +
Dig Mic

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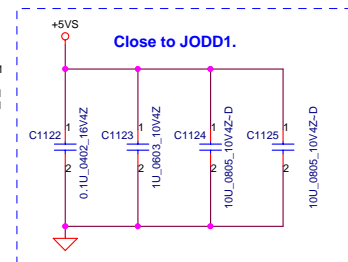
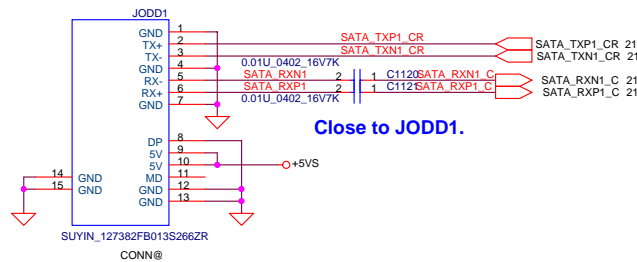




HDD Connector

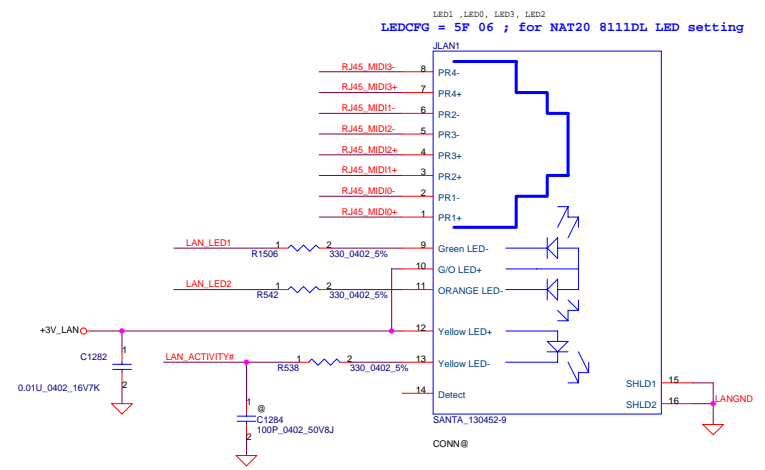
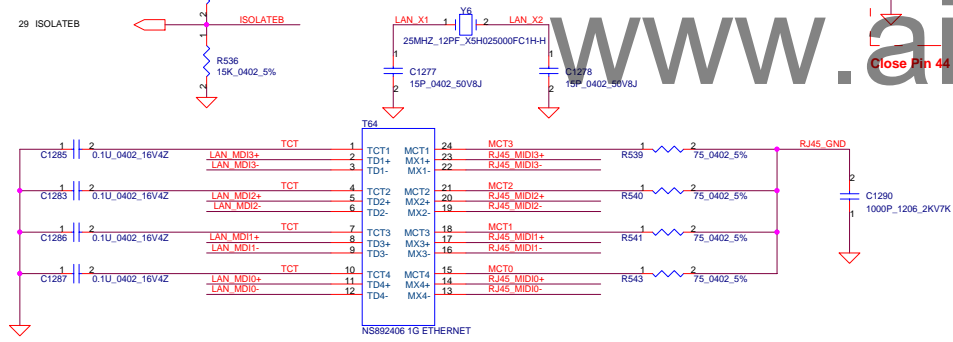
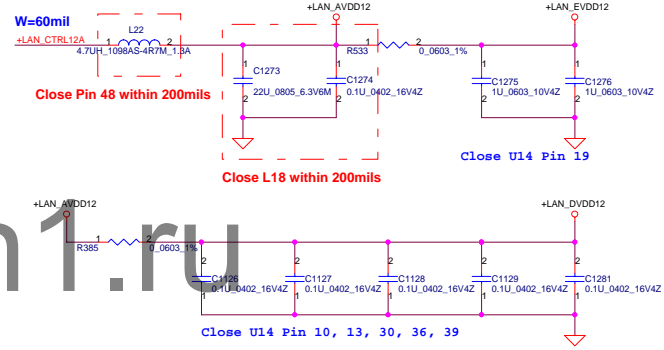
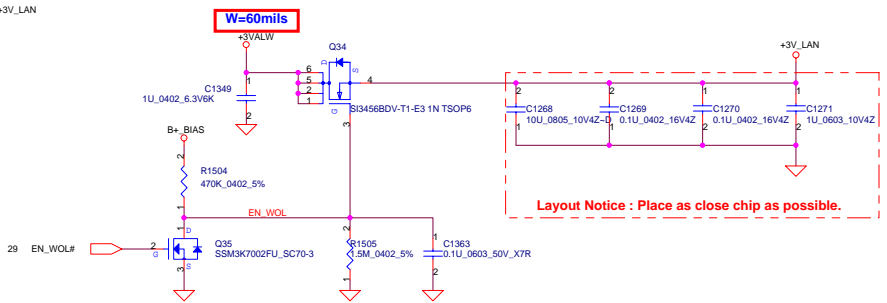
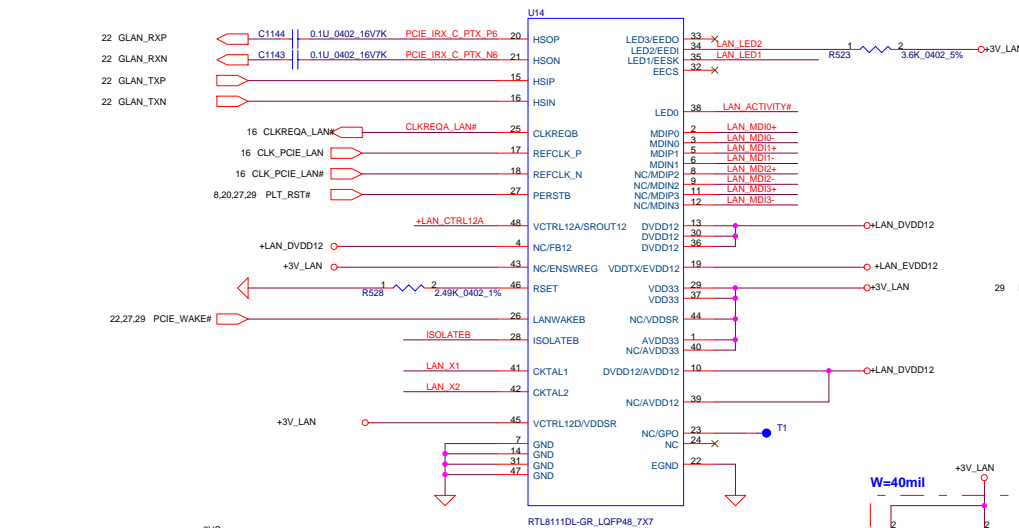


DVD-ROM Connector

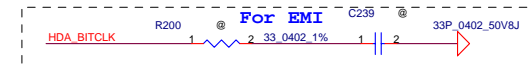


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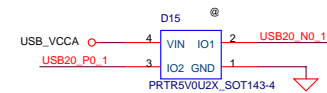
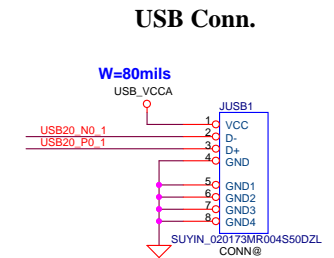
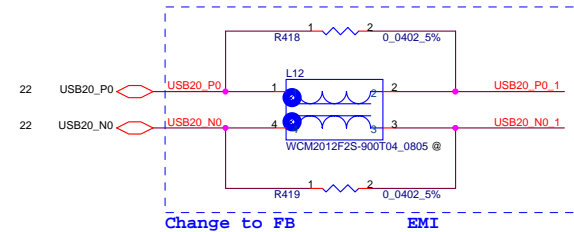
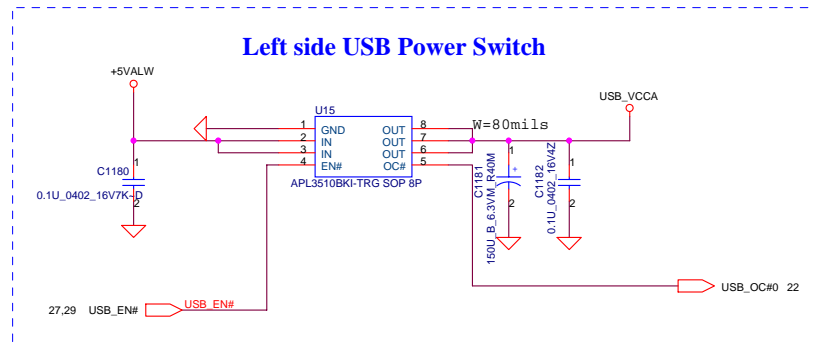


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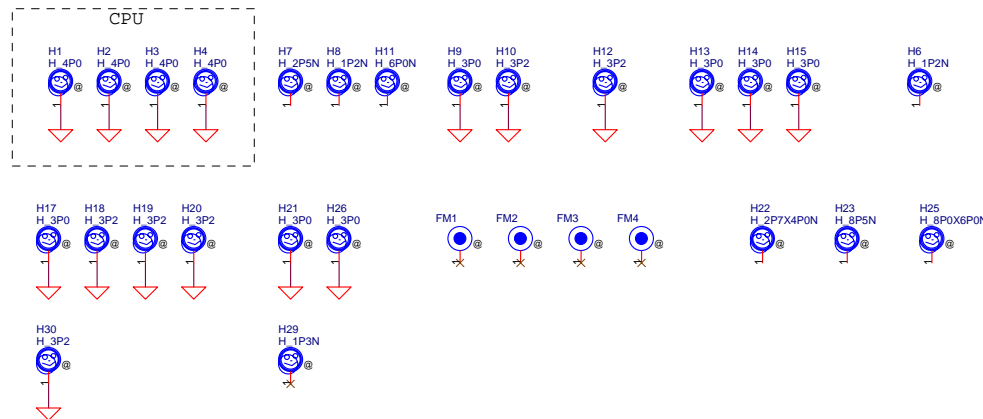


Blue Tooth Conn.

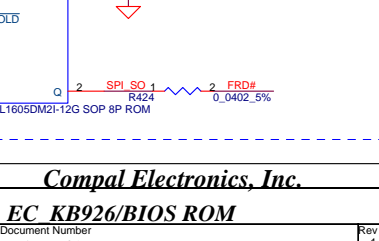
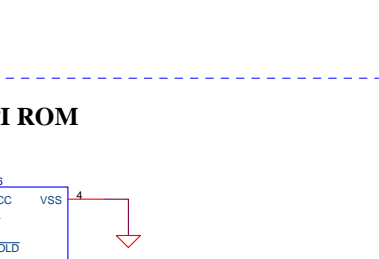
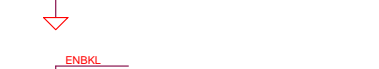
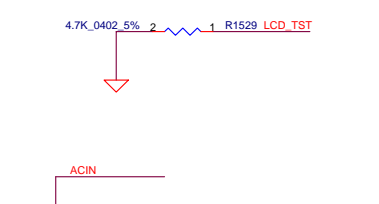
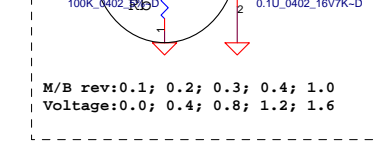
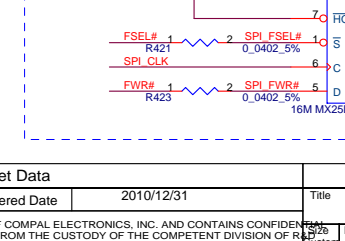
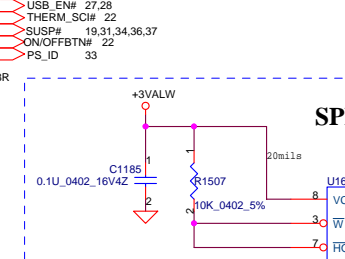
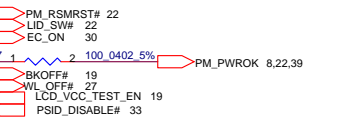
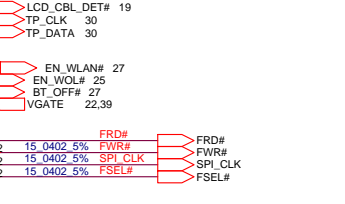
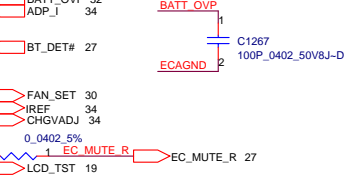
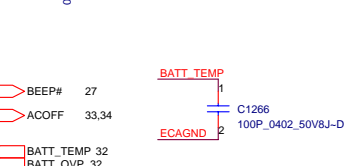
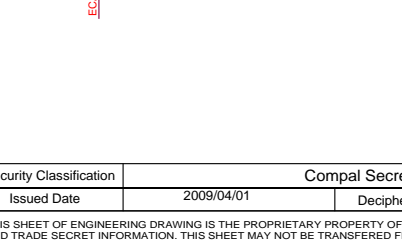
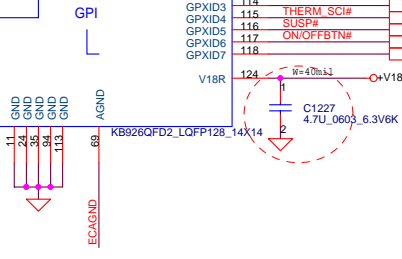
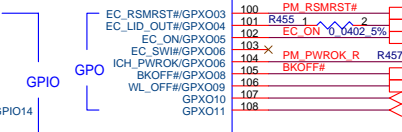
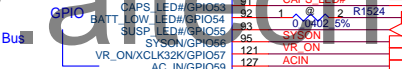
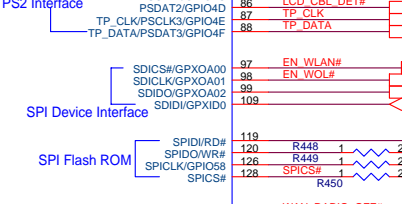
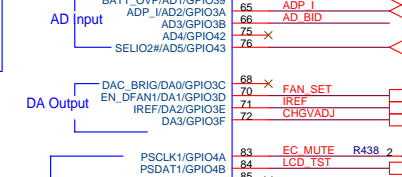
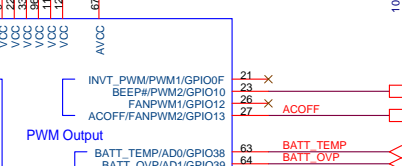
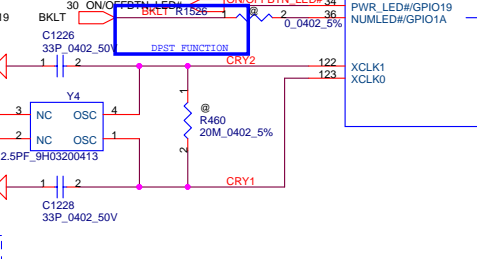
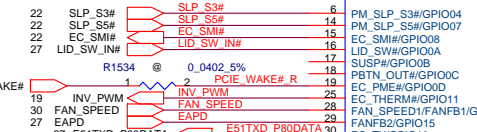
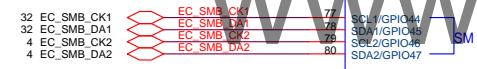
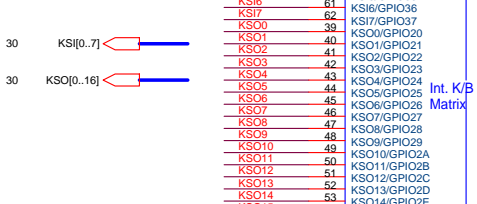
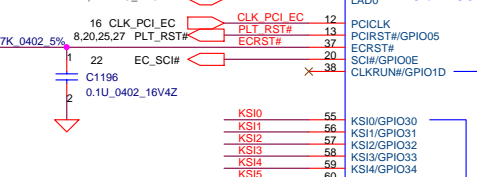
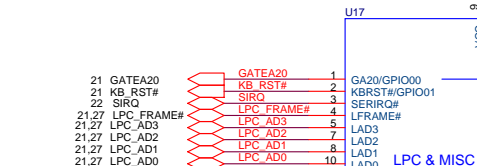
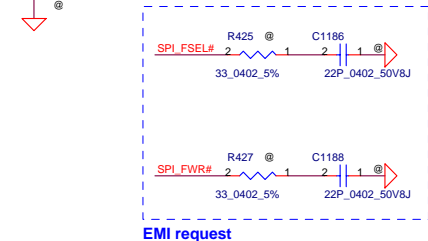
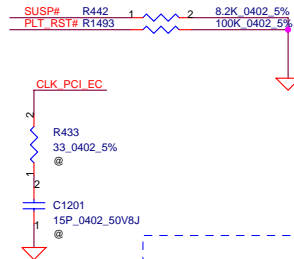
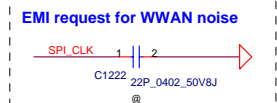
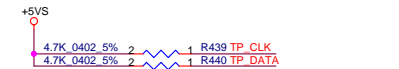
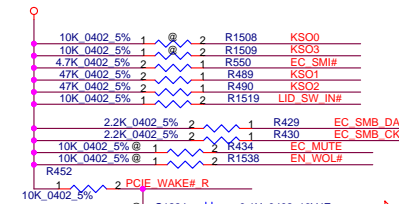
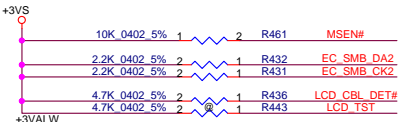
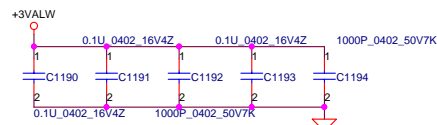
Security Classification		Compal Secret Data		Compal Electronics, Inc. BTB/Blue Tooth Connector	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title	Rev 1.0
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				Date:	Thursday, September 10, 2009
				Sheet	27 of 43



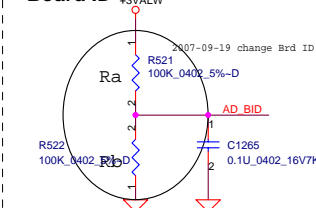
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Issued Date	2009/04/01	Deciphered Date	2010/12/31	USB/Screws	
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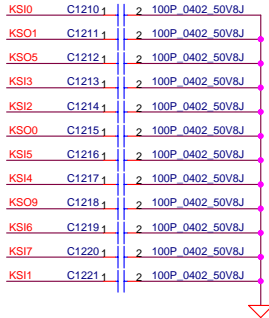
Board ID



M/B rev:0.1; 0.2; 0.3; 0.4; 1.0
Voltage:0.0; 0.4; 0.8; 1.2; 1.6

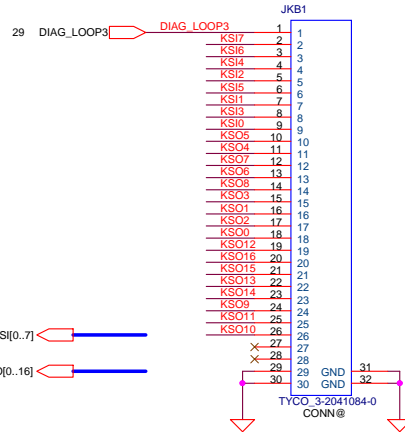
Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2009/04/01	Deciphered Date	2010/12/31	Title	EC KB926/BIOS ROM
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For EMI

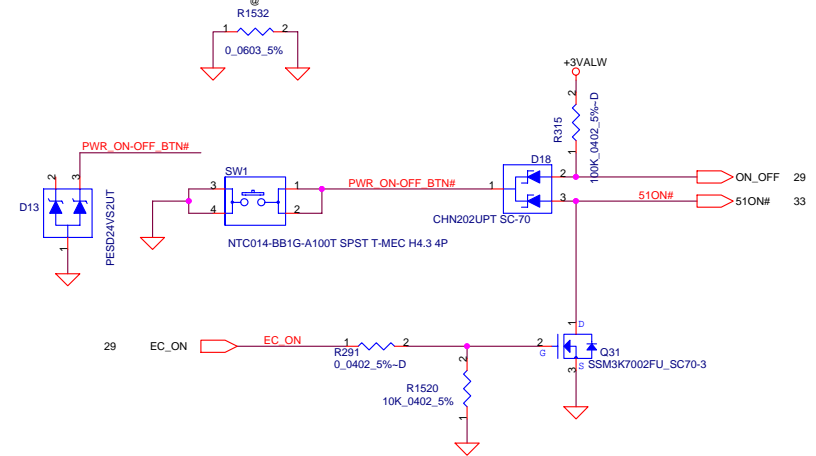


29 KSI[0..7]
29 KSO[0..16]

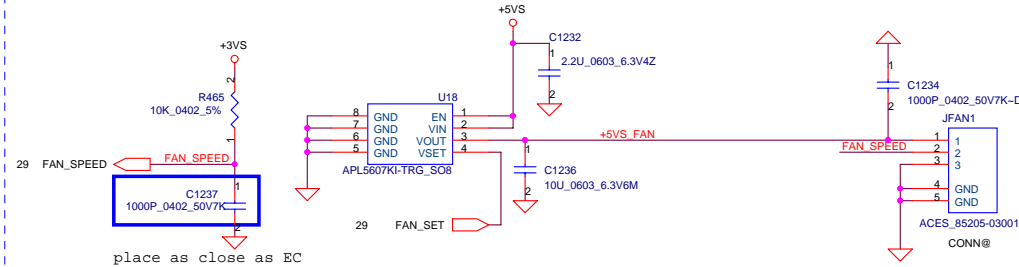
INT KBD Conn.



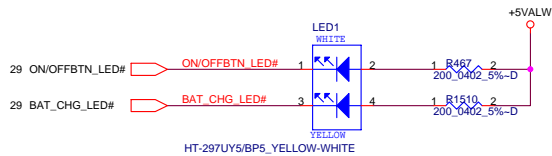
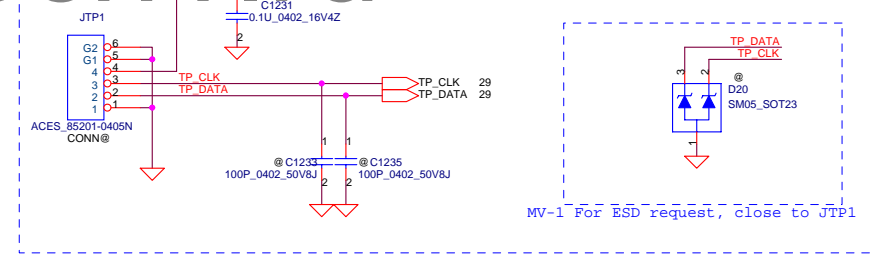
Power Button



Fan Control Circuit



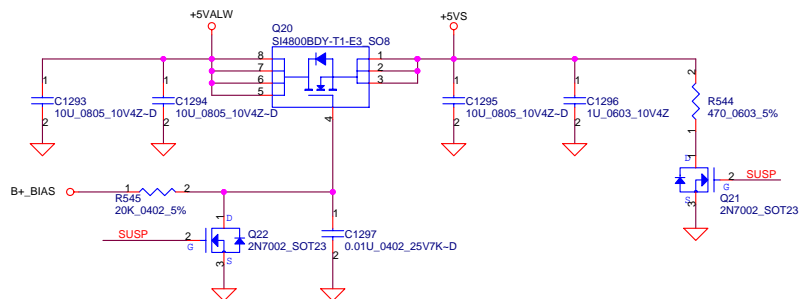
Touch Pad Connector



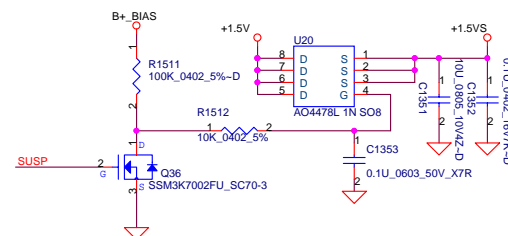
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						Document Number				Rev	
						LA-5541P				1.0	
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						Sheet				30 of 43	

A B C D E

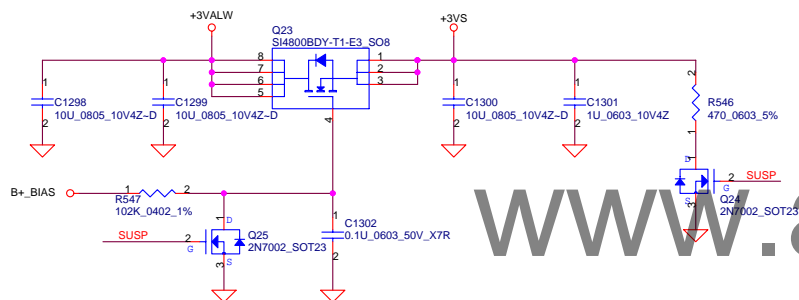
+5VALW to +5VS



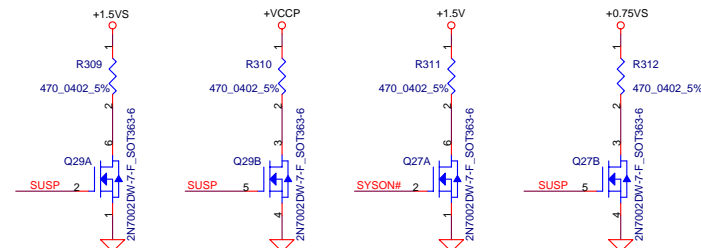
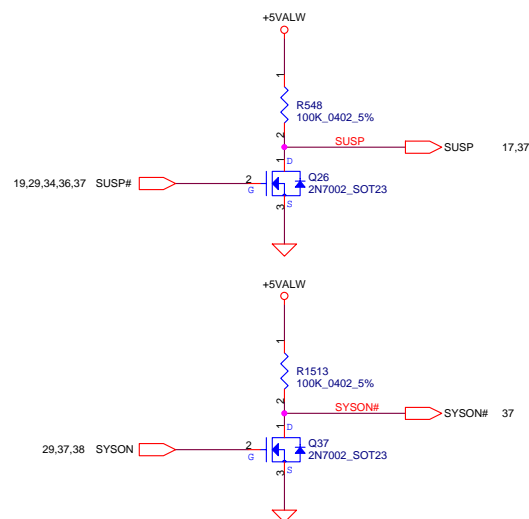
+1.5V To +1.5VS



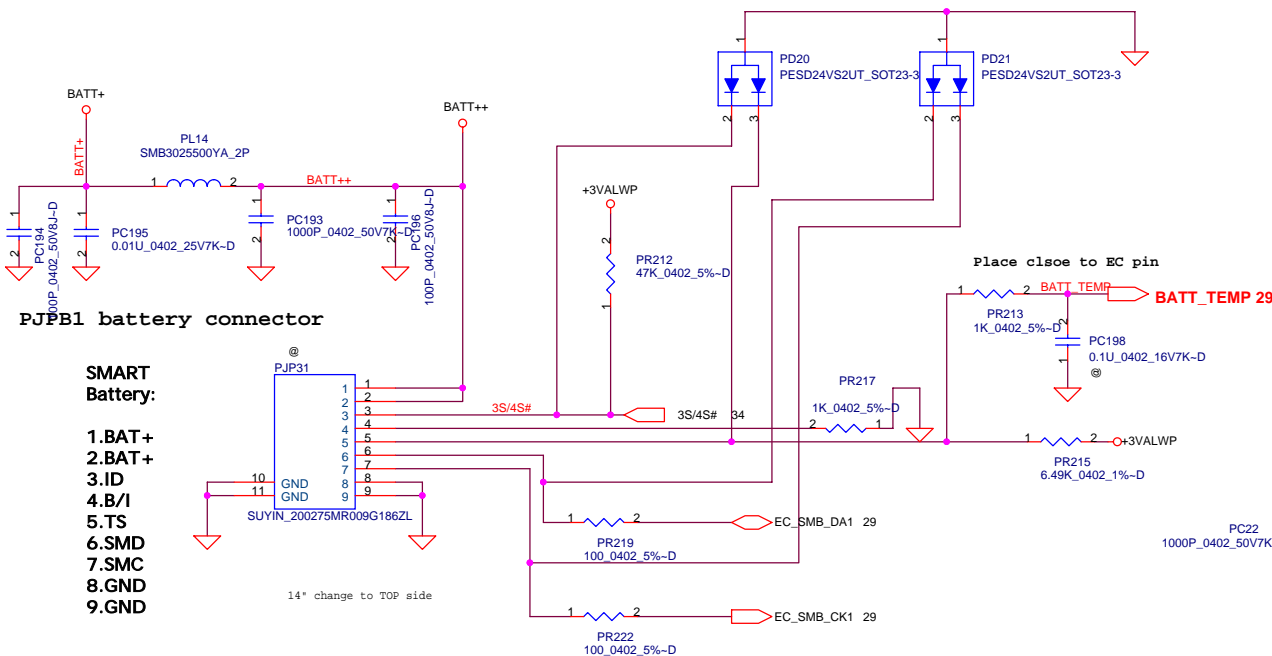
+3VALW to +3VS



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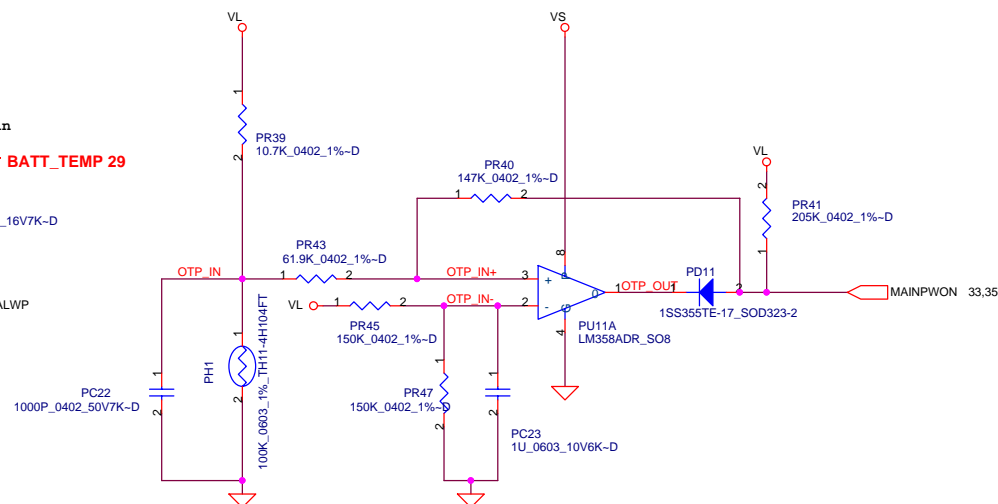


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						Document Number			
						LA-5541P			
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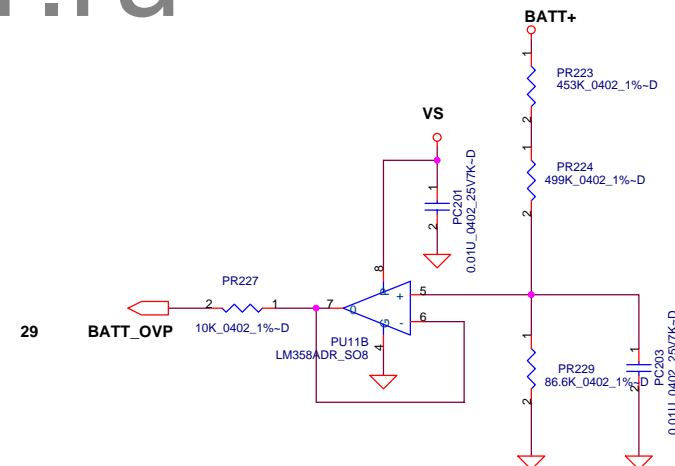
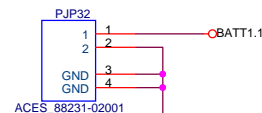
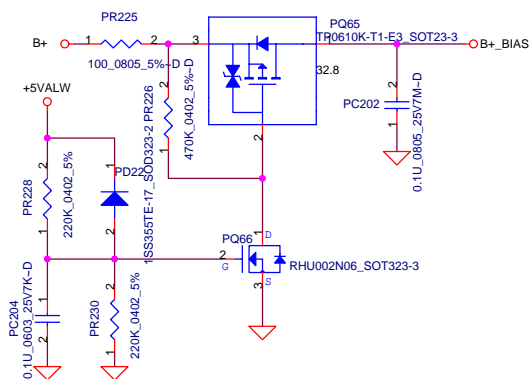
CPU OTP

PH1 under CPU botten side :
CPU thermal protection at 90 +-3 degree C
Recovery at 50 +-3 degree C



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COIN RTC Battery

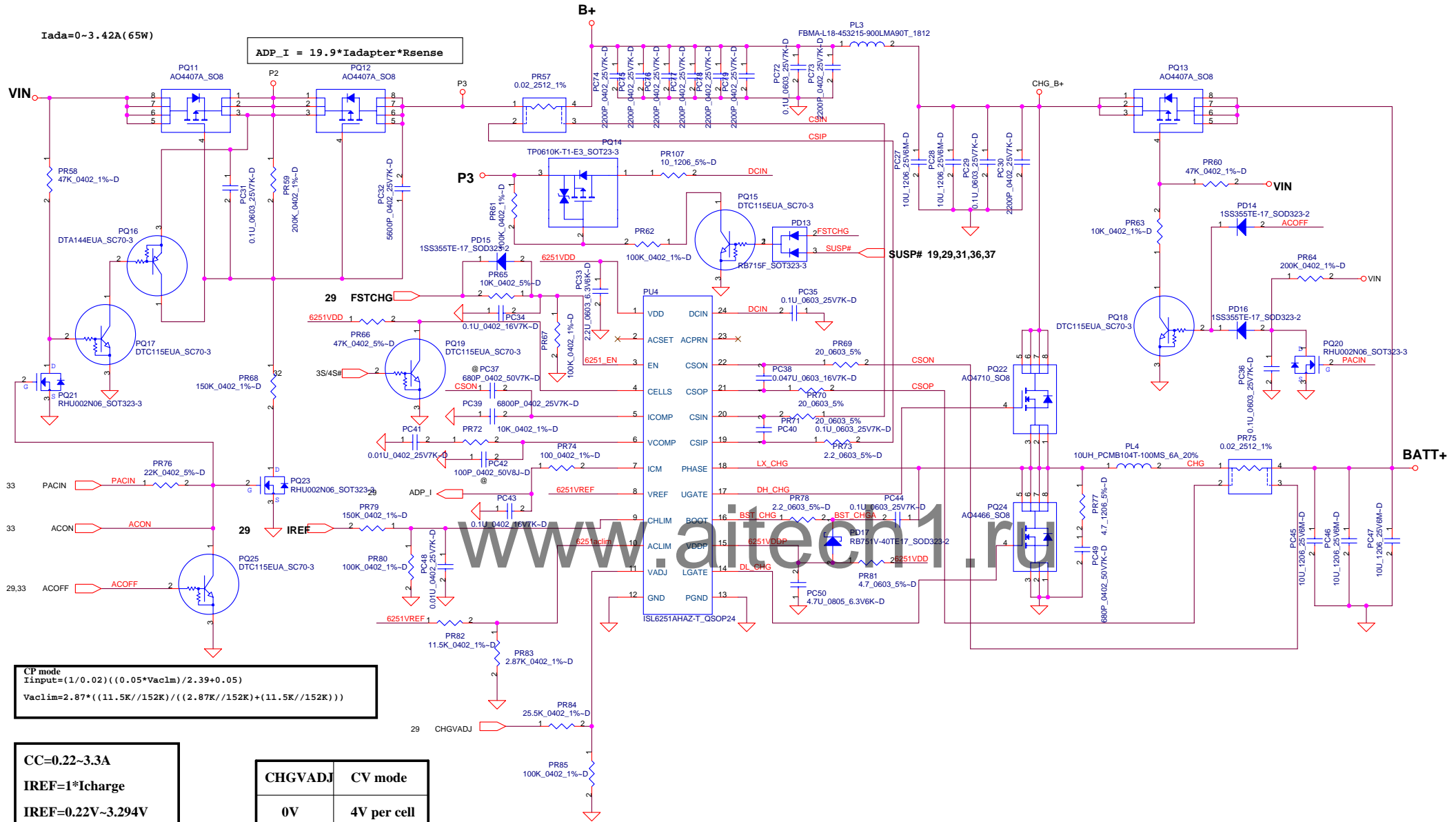


LI-3S :13.5V----BATT-OVP=1.126V
LI-4S :18V----BATT-OVP=1.5V
BATT-OVP=0.08338*BATT+

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I_{ada}=0~3.42A(65W)

$$ADP_I = 19.9 \cdot I_{\text{adapter}} \cdot R_{\text{sense}}$$

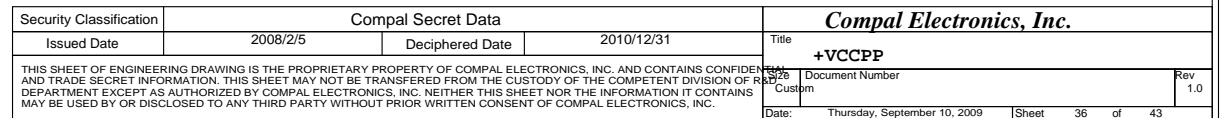


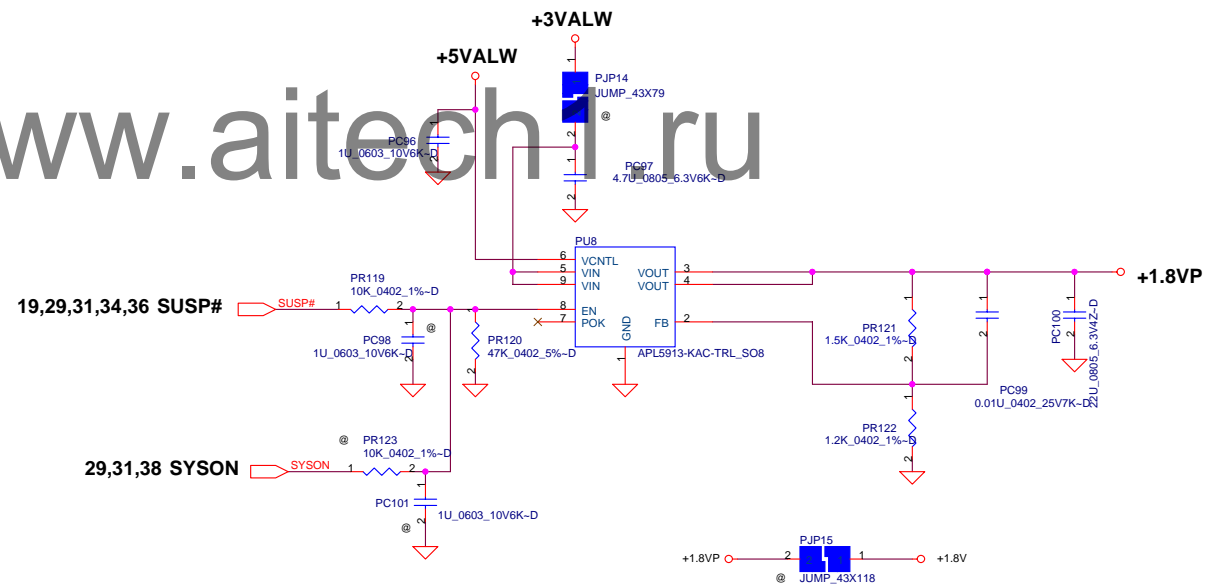
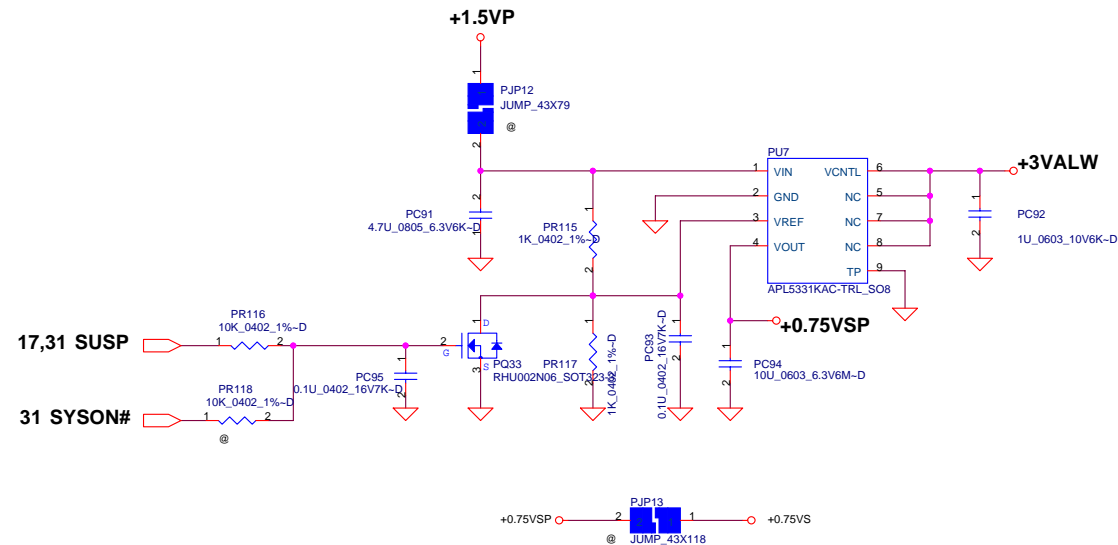
CP mode
 $I_{\text{input}} = (1/0.02) \cdot ((0.05 \cdot V_{\text{aclm}}) / 2.39 + 0.05)$
 $V_{\text{aclm}} = 2.87 \cdot ((11.5K / 152K) / ((2.87K / 152K) + (11.5K / 152K)))$

CC=0.22~3.3A
 IREF=1*I_{charge}
 IREF=0.22V~3.294V

CHGVADJ	CV mode
0V	4V per cell
1.882V	4.2V per cell
3.294V	4.45V per cell

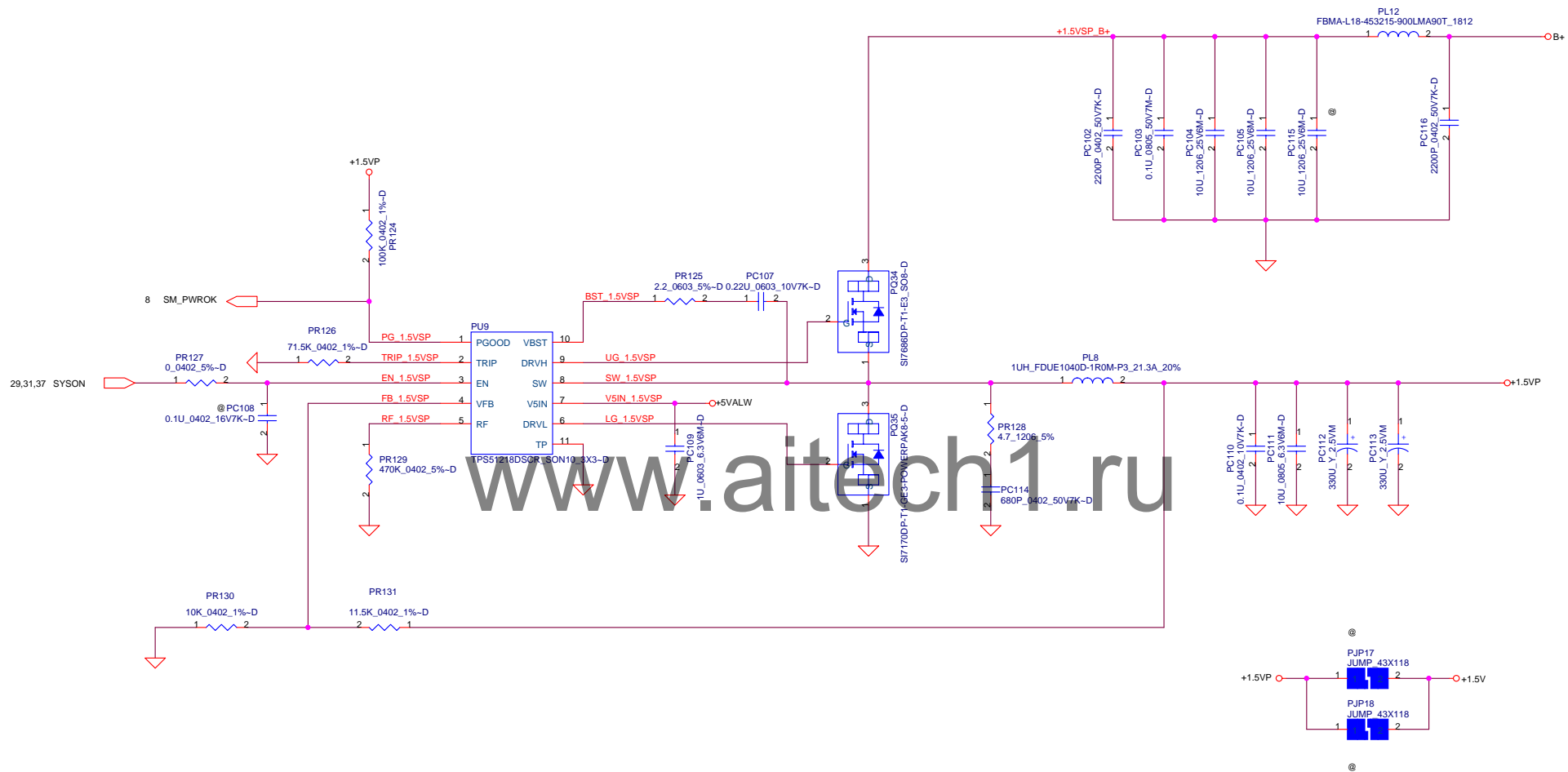
F_{SW}=290KHZ





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F_{SW}=300KHZ



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<i>Item</i>	<i>Page#</i>	<i>Title</i>	<i>Date</i>	<i>Request Owner</i>	<i>Issue Description</i>	<i>Solution Description</i>	<i>Rev.</i>
1	39	CPU_CORE	09' 09/09	Compal_Will	Modify CPU load line	PR170 change 6.65K_1\$_0402 to 9.1K_1\$_0402	0.5
www.aitech1.ru							

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Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
1	21, 23		2009/06/05	Compal	HDMI NO SOUND	ADD R319, R320, R323, R321 DEL R322, R324, R92	0.2
2	29, 30		2009/06/05	Compal	Battery LED can't work	ADD NET BAT_CHG_LED#	0.2
3	19		2009/06/05	Compal		Change JLVDS connect	0.2
4	24		2009/06/05	Compal		Change JODD1 connect	0.2
5	17		2009/06/05	Compal		Change Level shift IC to ASMEDIA	0.2
6	16		2009/06/18	Compal		reserve R1535 to using LOW POWER CLK GEN RTM890-397	0.3
7	26		2009/06/24	Compal		DEL Y5 C1244, C1246	0.3
8	19		2009/07/08	Compal		reserve Q39,Q40,R1536, R1537, C1359, C1360, C1361, C1362	0.3
9	30		2009/07/08	Compal		Del C313 D14	0.3
10	29,30		2009/07/08	Compal		Add EAPD trace	0.3
11	25		2009/07/23	Compal		Add C1363	1.0
12	30		2009/07/23	Compal		Change Keyboard footprint	1.0
13							
14							
15							
16							
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5

4

3

2

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X76



PCB-MB



PENRYN SFF_UFCBGA956
723@



PENRYN SFF_UFCBGA956
SU3500@



PENRYN SFF_UFCBGA956
SU9400@



PENRYN SFF_UFCBGA956
SU4100@



PENRYN SFF_UFCBGA956
SU7300@



U5_R3@



PENRYN SFF_UFCBGA956
R3_SU3500@



PENRYN SFF_UFCBGA956
R3_SU9400@



U3_R3@



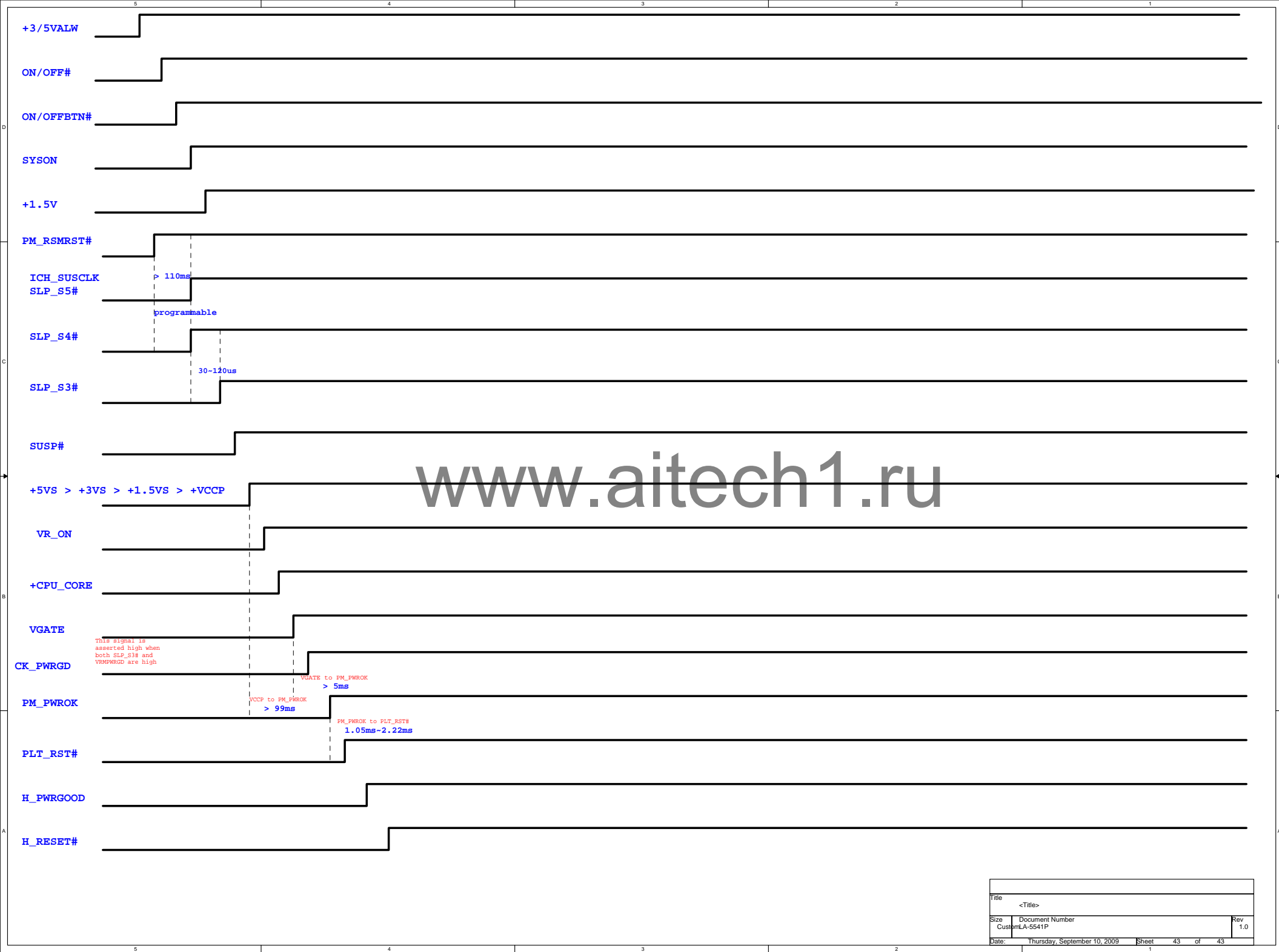
PENRYN SFF_UFCBGA956
R3_SU4100@



PENRYN SFF_UFCBGA956
R3_SU7300@

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Title			
<Title>			
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